

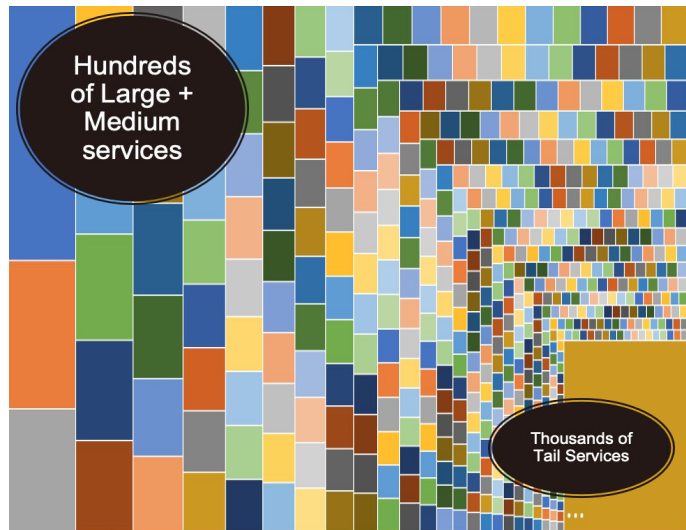
DCPerf: An Open-Source, Battle-Tested Performance Benchmark Suite for Datacenter Workloads

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We work on hyperscale, fast-growing fleet



- 30+ regions with millions of servers
- Billions of users

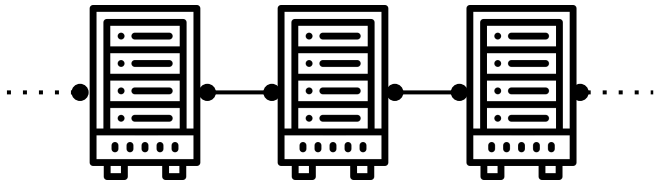


- Multiple server types - Compute, Storage & AI
- 1000s of workloads
- Service oriented architecture

Every compute roadmap decision needs to be carefully evaluated

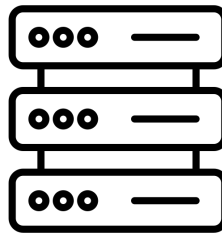
Perf evaluation of new HW in production is challenging

What a production service requires



- Racks in production datacenters, 100s of servers
- Meta-managed secure environments for live traffic
- Proprietary tools and software stack

What's available at early-stage...



- Simulators, emulators, a few engineering samples
- Reference boards, generic OS and software stack
- No access to production code and data

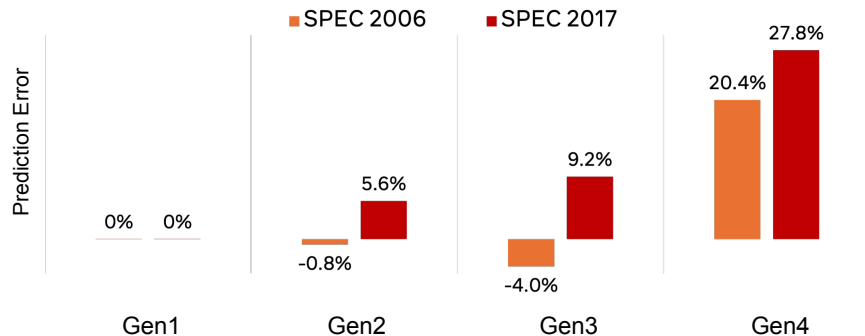
Highly representative benchmarks are a must!

Standardized benchmarks are not sufficient

- Accuracy is the primary concern

Further, they do not

- Support scalable application architecture
- Use diverse software libraries
- Reflect system arch and uArch behaviors
- Evolve over time



Prediction error in gen-over-gen performance between “SPEC vs. Prod”

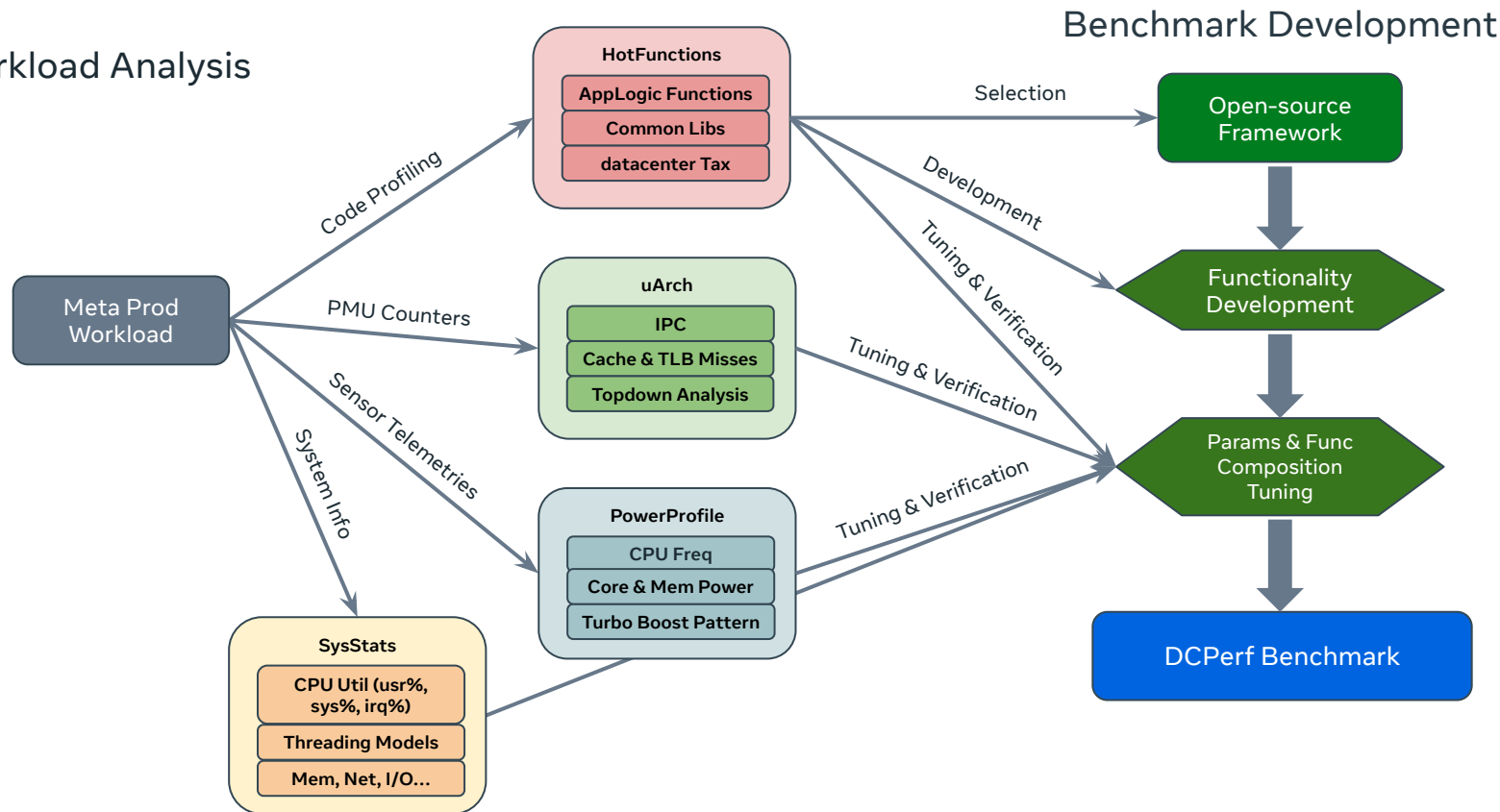
DCPerf is designed to address these problems!

DCPerf benchmark suite

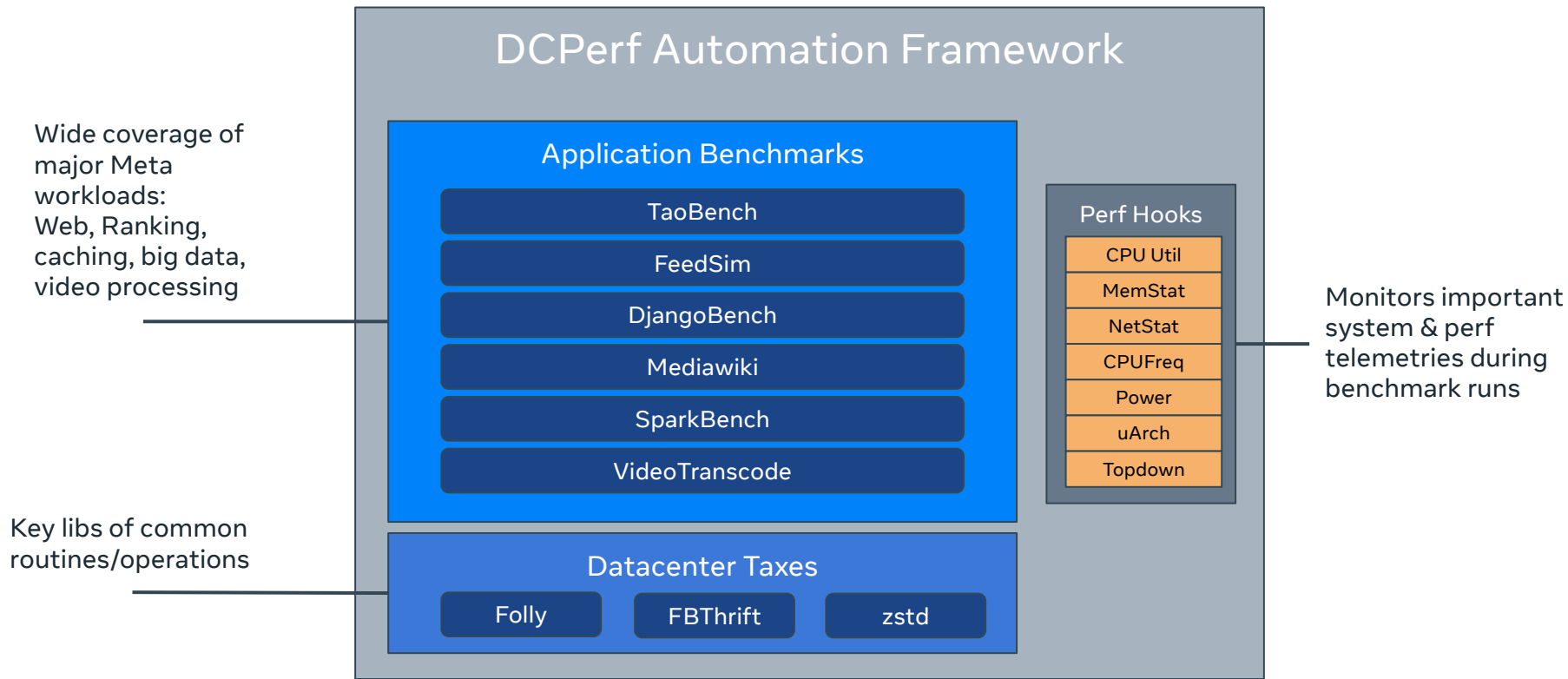
- Designed to representing major hyperscale workloads at Meta datacenters
 - Leverage open-source software for ease of use outside Meta
 - Modular, parameterized and scalable - similar to distributed hyperscalar applications
 - Intended to be run on modern processors with high core counts, latest OS and kernels.
- Designed for
 - Hardware performance evaluations
 - Early-stage software optimizations and hardware pathfinding
 - Hardware-software co-design
 - Open source to enable collaboration with industry and academic partners

DCPerf design process

Workload Analysis

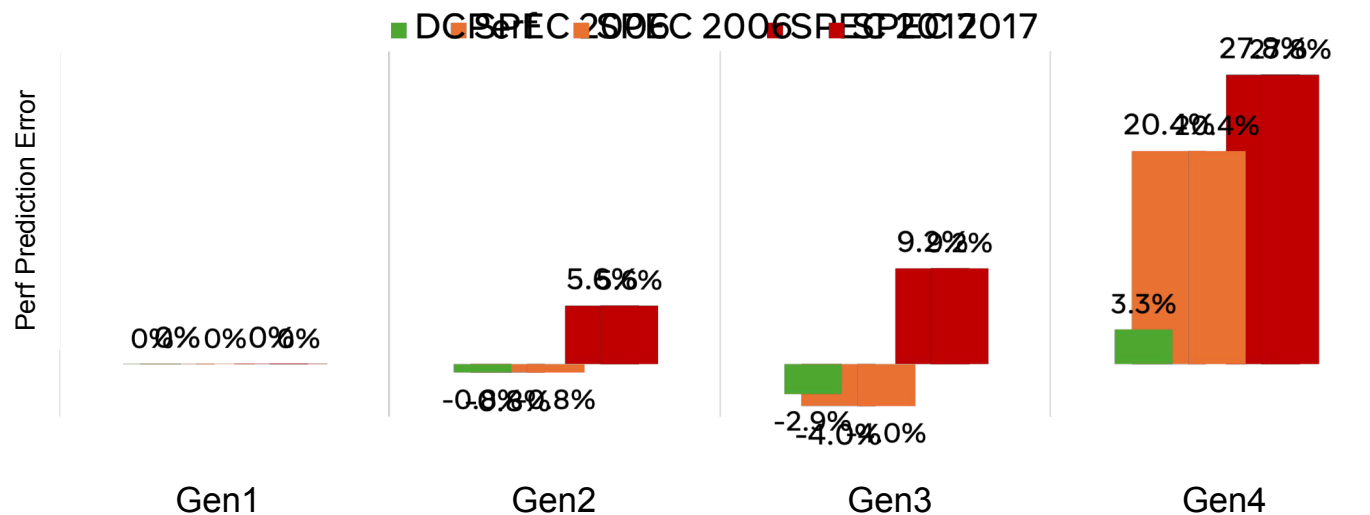


DCPerf components



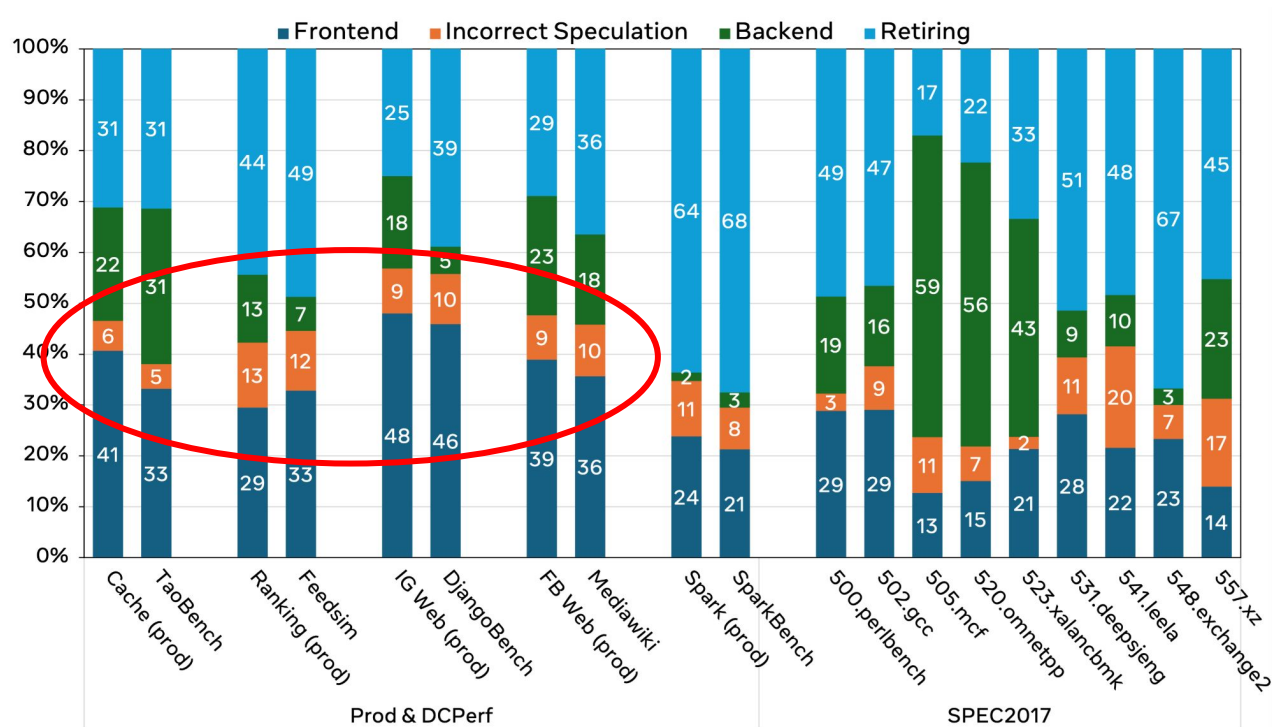
DCPerf supports x86/ARM, CentOS/Ubuntu and latest Linux versions

Validating DCPperf: Predicting workload performance



DCPperf can project Prod Services' performance more accurately

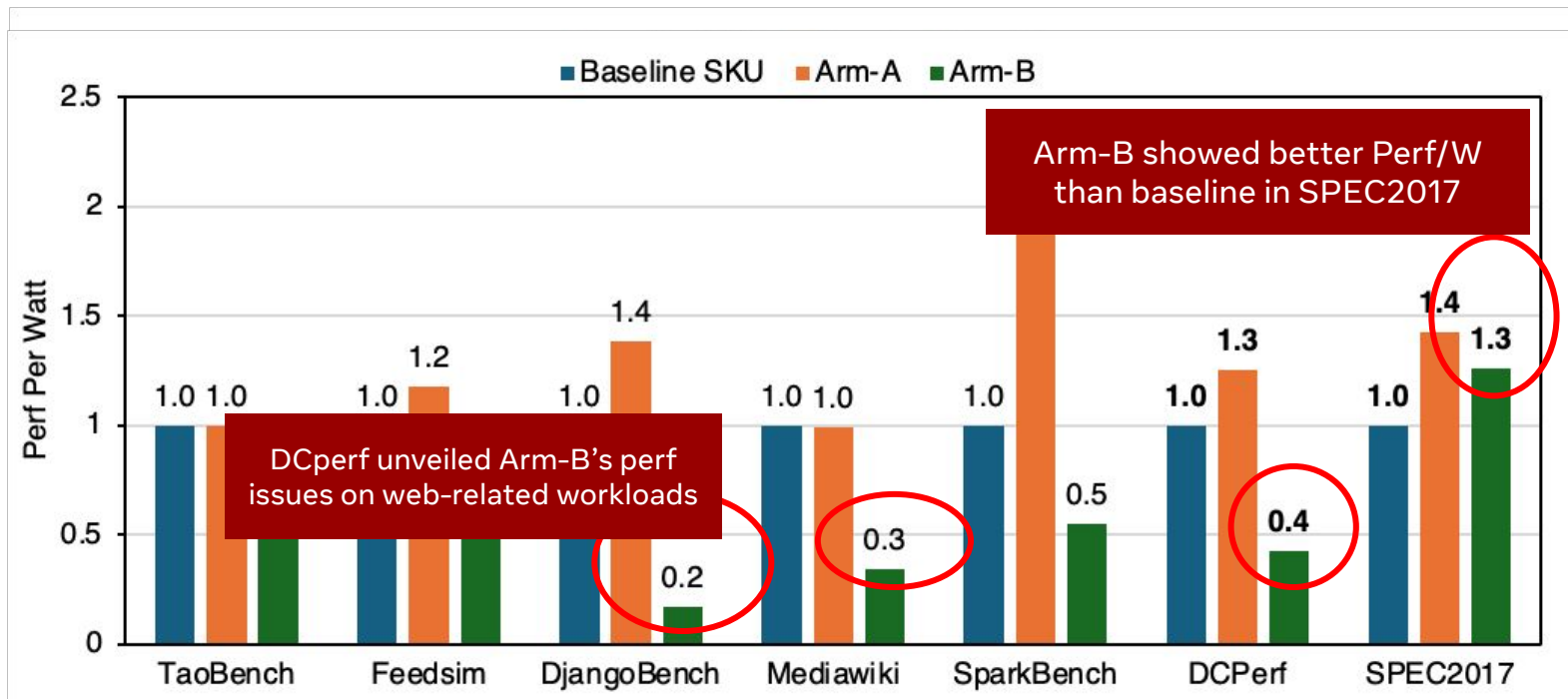
Validating DCPperf: Representing topdown metrics



More metrics (uArch, hot functions, power etc) in the paper!

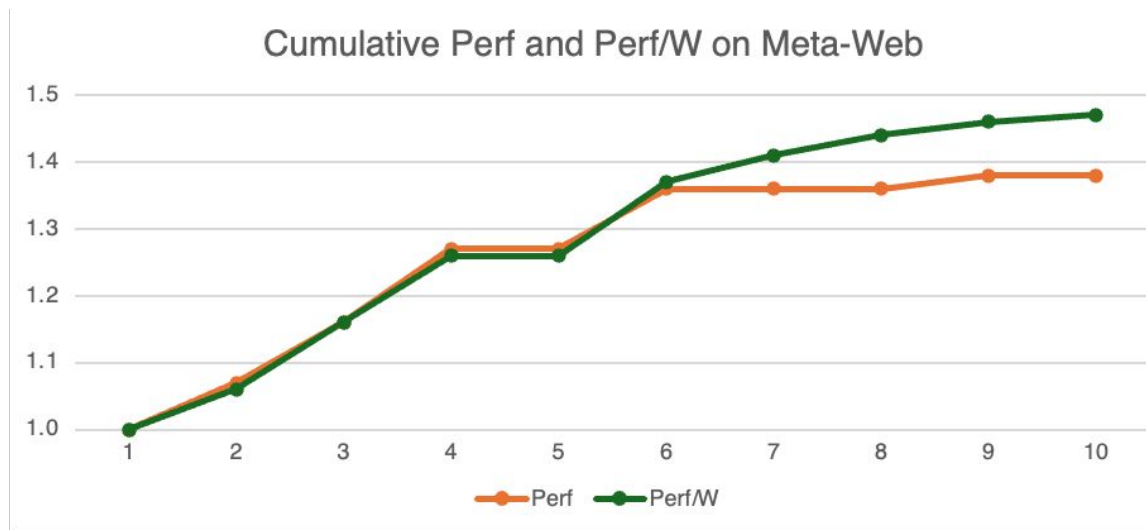
Case 1: ARM CPU decision

- First-gen Arm-based server at Meta, we needed to select between two options.
- DCPerf data helped make the decision deploy ARM-A @scale in the fleet



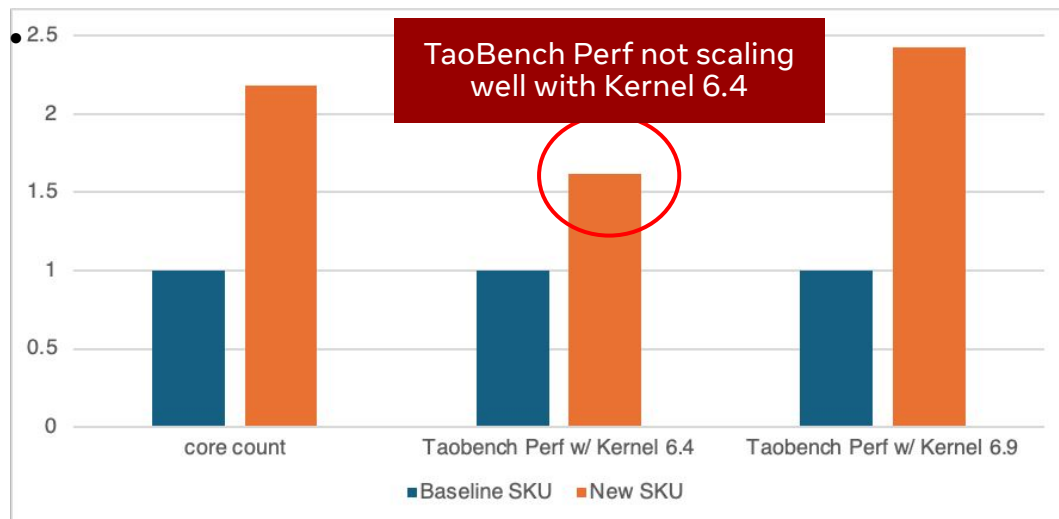
Case 2: Guiding uarch optimizations

- Worked with a vendor to tune one of Meta's large workloads on a new CPU
 - Used Mediawiki as a proxy to improve performance with vendor
 - After iterating over few weeks, we delivered **48% Perf/W improvement**
 - Insights and improvements did **NOT** show up on SPEC CPU



Case 3: Linux kernel improvement

- Tested a new CPU SKU which very high core count
- Ran TaoBench on Linux 6.4 => scheduler issues
- Issue was not seen on targeted microbenchmarks
- **Fast** kernel perf issue detection and optimization.



New SKU on Kernel 6.4

Samples: 6M of event 'cycles', Event count (approx.): 4997146868445

Overhead	Symbol
16.93%	[k] update_cfs_group
13.91%	[k] enqueue_entity
7.99%	[k] enqueue_task_fair
3.44%	[k] pick_next_task_fair
2.91%	[k] dequeue_task_fair.llvm.1744306887491663898
2.82%	[k] __update_load_avg_cfs_rq
2.56%	[k] dequeue_entity
2.44%	[k] __update_load_avg_se
1.38%	[k] try_to_wake_up.llvm.11023368715565402039
1.08%	[k] set_next_entity
1.08%	[k] native_sched_clock
1.06%	[k] __tcp_push_pending_frames
1.01%	[k] read_tsc

High overhead in kernel scheduling functions

New SKU on Kernel 6.9

Samples: 7M of event 'cycles', Event count (approx.): 4048436950878

Overhead	Symbol
8.20%	[k] acpi_processor_ffh_cstate_enter
4.46%	[k] enqueue_entity
4.16%	[k] dequeue_task_fair.llvm.10053004214620959914
2.20%	[k] switch_fpu_return
2.19%	[k] native_sched_clock
2.14%	[k] read_tsc
1.83%	[k] _copy_to_iter
1.70%	[k] select_task_rq_fair
1.65%	[k] pick_next_task_fair
1.46%	[k] tcp_rcv_established
1.44%	[k] save_fpregs_to_fpstate
1.40%	[.] MurmurHash3_x86_32

Much lower kernel side overhead on 6.9

DCPerf keeps evolving



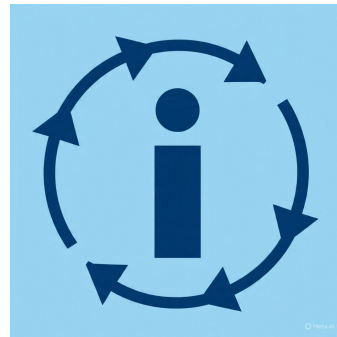
Workload coverage

New benchmarks for emerging AI applications



DCPerf Mini

Shrunk version with short execution time for silicon exploration (simulation/emulation)



Timely refresh

Keeping up with the emerging HW & SW trends

Conclusion

- DCPperf is a representative benchmark suite for our datacenter applications
- It is validated using production workloads and guides procurement decisions and codesign for millions of servers
- We made it open-source to foster community innovation
 - Contributions from other hyperscalars and the broader community are welcome!

